

**PATENT NUMBER and  
ISSUE DATE**

**U.S. UTILITY Patent Application**

APPL NUM 10005062	FILING DATE 12/03/2001	CLASS 327	SUBCLASS 16	GAU 2846	EXAMINER <i>Barlowksi</i>
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\*\*APPLICANTS: Watkins Daniel; *2825*

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials		ATTORNEY DOCKET NO LS1-01-651	
TITLE : Method and system for implementing incremental change to circuit design U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

**NOTICE OF ALLOWANCE MAILED**

**ISSUE FEE**

Amount Due	Date Paid

TERMINAL

**DISCLAIMER**

**Assistant Examiner**

**Primary Examiner**

**PREPARED FOR ISSUE**

**CHARGES ALLOWED**

Total Claims      Print Claims for  
D.O.

**DRAWING**

Sheets Drawn      Pgs Drawn      Print Pg

**Application Examiner**

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